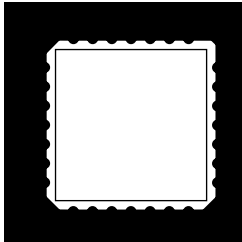


# SURFACE MOUNT POSITIVE ADJUSTABLE VOLTAGE REGULATOR



**Three Terminal, Adjustable Voltage, 1.0 Amp Precision Positive Regulator In A Hermetic Surface Mount Package**

## FEATURES

- Hermetic Surface Mount Package
- Adjustable Output Voltage
- Built-In Thermal Overload Protection
- Short Circuit Current Limiting
- Product Is Available Hi-Rel Screened
- Electrically Similar To Industry Standard Type LM117HV

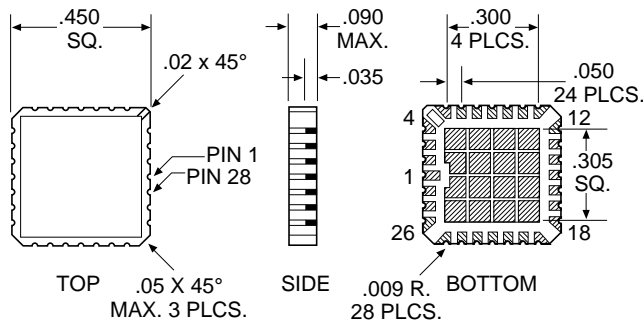
## DESCRIPTION

This three terminal positive regulator is supplied in a hermetically sealed surface mount package. All protective features are designed into the circuit, including thermal shutdown, current limiting and safe-area control. With heat sinking, they can deliver 1.0 amp of output current. This unit features output voltages that can be trimmed using external resistors, from 1.2 volts to 57 volts.

## ABSOLUTE MAXIMUM RATINGS @ 25°C

Power Dissipation ( $P_D$ ) (Internally Limited).....	10 W
Input - Output Voltage Differential .....	60 V
Operating Junction Temperature Range .....	- 55°C to + 150°C
Storage Temperature Range .....	- 65°C to + 150°C
Lead Temperature (Soldering 10 Seconds).....	280°C
Thermal Resistance: Junction-to-Case.....	12°C/W

## MECHANICAL OUTLINE



### Pin Connection

Pin 1, 15 thru 28: IN  
 Pin 2, 3, 13, and 14: ADJ  
 Pin 4 thru 12: OUT

3.5

**ELECTRICAL CHARACTERISTICS**  $-55^{\circ}\text{C} \leq T_A \leq 125^{\circ}\text{C}$ ,  $I_L = 8\text{mA}$  (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Max.	Unit
Reference Voltage	$V_{REF}$	$V_{DIFF} = 3.0\text{V}$ , $T_A = 25^{\circ}\text{C}$ $V_{DIFF} = 3.3\text{V}$ • $V_{DIFF} = 40\text{V}$ • $V_{DIFF} = 60\text{V}$ •	1.20 1.20 1.20 1.20	1.30 1.30 1.30 1.30	V
Line Regulation (Note 1)	$R_{LINE}$	$3.0\text{V } V_{DIFF} \ 40\text{V}$ , $V_{OUT} = V_{ref}$ , $T_A = 25^{\circ}\text{C}$ $3.3\text{V } V_{DIFF} \ 40\text{V}$ , $V_{OUT} = V_{ref}$ • $40\text{V } V_{DIFF} \ 60\text{V}$ , $V_{OUT} = V_{ref}$ , $T_A = 25^{\circ}\text{C}$ $40\text{V } V_{DIFF} \ 60\text{V}$ , $V_{OUT} = V_{ref}$ •	-12 -25 -8 -12	12 25 8 12	mV
Load Regulation (Note 1)	$R_{LOAD}$	$V_{DIFF} = 3.0\text{V}$ , $10\text{mA}$ $I_L \ 1.0\text{A}$ , $T_A = 25^{\circ}\text{C}$ $V_{DIFF} = 3.3\text{V}$ , $10\text{mA}$ $I_L \ 1.0\text{A}$ • $V_{DIFF} = 40\text{V}$ , $10\text{mA}$ $I_L \ 300\text{mA}$ , $T_A = 25^{\circ}\text{C}$ $V_{DIFF} = 40\text{V}$ , $10\text{mA}$ $I_L \ 195\text{mA}$ • $V_{DIFF} = 60\text{V}$ , $10\text{mA}$ $I_L \ 30\text{mA}$ •	-20 -20 -20 -20 -20	20 20 20 20 20	mV
Thermal Regulation	$V_{RTH}$	$V_{IN} = 14.6\text{V}$ , $I_L = 1.0\text{A}$ $P_d = 20\text{ Watts}$ , $t = 20\text{ ms}$ , $T_A = 25^{\circ}\text{C}$	-16	16	mV
Ripple Rejection (Note 2)	$R_N$	$f = 120\text{ Hz}$ , $V_{OUT} = V_{ref}$ • $C_{Adj} = 10\ \mu\text{F}$ , $I_{OUT} = 100\text{ mA}$	66		dB
Adjustment Pin Current	$I_{Adj}$	$V_{DIFF} = 3.0\text{V}$ , $T_A = 25^{\circ}\text{C}$ $V_{DIFF} = 3.3\text{V}$ • $V_{DIFF} = 40\text{V}$ • $V_{DIFF} = 60\text{V}$ •		100 100 100 100	$\mu\text{A}$
Adjustment Pin Current Change	$I_{Adj}$	$V_{DIFF} = 3.0\text{V}$ , $10\text{mA}$ $I_L \ 1.0\text{A}$ , $T_A = 25^{\circ}\text{C}$ $V_{DIFF} = 3.3\text{V}$ , $10\text{mA}$ $I_L \ 1.0\text{A}$ • $V_{DIFF} = 40\text{V}$ , $10\text{mA}$ $I_L \ 300\text{mA}$ , $T_A = 25^{\circ}\text{C}$ $V_{DIFF} = 40\text{V}$ , $10\text{mA}$ $I_L \ 195\text{mA}$ • $3.0\text{V } V_{DIFF} \ 40\text{V}$ , $T_A = 25^{\circ}\text{C}$ $3.3\text{V } V_{DIFF} \ 40\text{V}$ • $3.3\text{V } V_{DIFF} \ 60\text{V}$ •	-10 -10 -10 -10 -10 -10 -10	10 10 10 10 10 10 10	$\mu\text{A}$
Minimum Load Current	$I_{Lmin}$	$V_{DIFF} = 3.0\text{V}$ , $V_{OUT} = 1.4\text{V}$ (forced) $V_{DIFF} = 3.3\text{V}$ , $V_{OUT} = 1.4\text{V}$ (forced) • $V_{DIFF} = 40\text{V}$ , $V_{OUT} = 1.4\text{V}$ (forced) • $V_{DIFF} = 60\text{V}$ , $V_{OUT} = 1.4\text{V}$ (forced) •		10 10 10 10	mA
Current Limit (Note 2)	$I_{CL}$	$V_{DIFF} = 40\text{V}$ , $T_A = 25^{\circ}\text{C}$ $V_{DIFF} = 60\text{V}$ , $T_A = 25^{\circ}\text{C}$	0.3 0.05	1.0 0.40	A

**Notes:**

- Load and Line Regulation are specified at a constant junction temperature. Pulse testing with low duty cycle is used. Changes in output voltage due to heating effects must be taken into account separately.
- If not tested, shall be guaranteed to the specified limits.
- The • denotes the specifications which apply over the full operating temperature range.